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UTILITY PATENT APPLICATION TRANSMITTAL

Attorney Docket No. MI22-1376

First Inventor or Application Identifier Kevin J. Torek et al.

Title Methods of Removing at Least Some of A Material from...

(Only for new nonprovisional applications under 37 C.F.R. § 1.53(b))

Express Mail Label No. 77 ARSB86385

APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents.	Assistant Commissioner for Patents ADDRESS TO: Box Patent Application Washington, DC, 20231
1. X *Fee Transmittal Form (e.g., PTO/SB/17) (Submit an original and a duplicate for fee processing) 2. X Specification [Total Pages 20 1 1 1 1 1 1 1 1 1	5. Microfiche Computer Program (Appendix) 6. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary) e page a. Computer Readable Copy b. Paper Copy (identical to computer copy) c. Statement verifying identity of above copies
- Background of the Invention - Brief Summary of the Invention - Brief Description of the Drawings (if filed) - Detailed Description - Claim(s) - Abstract of the Disclosure 3. X Drawing(s) (35 U.S.C. 113) [Total Sheets 2] 4. Oath or Declaration [Total Pages 2] a. X NEXECUTED a. X NEXECUTED b. Copy from a prior application (37 C.F.R. § 1.63 (for continuation/divisional with Box 16 completed) i. DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application see 37 C.F.R. §§ 1.63(d)(2) and 1.33(b). *NOTE FOR ITEMS 1 & 13 IN ORDER TO BE ENTITLED TO PAY SMALL ENTITY FEES, A SMALL ENTITY STATEMENT IS REQUIRED (37 C.F.R. § 1.28).	13. Statement(s) Statement filed in prior application Status still proper and desired 14. Certified Copy of Priority Document(s) (if foreign priority is claimed) 15. X Other: Check
under Box 4b, is considered a part of the disclosure of the accompar reference. The incorporation <u>can only</u> be relied upon when a portion	(CIP) of prior application No: Group / Art Unit: of the prior application, from which an oath or declaration is supplied hying continuation or divisional application and is hereby incorporated by has been inadvertently omitted from the submitted application parts.
17. CORRESPOND	ENCE ADDRESS
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Name (Pnnt/Type) David G. Latwesen, Ph.D.	Registration No. (Attorney/Agent) 38,533

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TRANSMITTAL for FY 1999

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TOTAL AMOUNT OF PAYMENT

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Application Number	Filed Herewith					
Filing Date						
First Named Inventor	Kevin J. Torek et al.					
Examiner Name						
Group / Art Unit						
Attorney Docket No.	MI22-1376					

METHOD OF PAYMENT (check one)	FEE CALCULATION (continued)	
The Commissioner is hereby authorized to charge indicated fees and credit any over payments to: Deposit Deposit	3. ADDITIONAL FEES Large Entity Small Entity Fee Fee Fee Fee Fee Code (\$) Code (\$)	Fee Paid
Account 23-0925	105 130 205 65 Surcharge-late filing fee or oath	0.00
Number Deposit Account Name Wells, St. John	127 50 227 25 Surcharge - late provisional filling fee or cover sheet.	0.00
X Charge Any Additional	139 130 139 130 Non-English specification	0.00
Fee Required Under 37 CFR 1.16 and 1.17	147 2,520 147 2,520 For filing a request for reexamination	0.00
	112 920* 112 920* Requesting publication of SIR prior to Examiner action	0.00
2. XXX Payment Enclosed: X Check Money Other	113 1,840* 113 1,840* Requesting publication of SIR after Examiner action	0.00
FEE CALCULATION	115 110 215 55 Extension for reply within first month	0.00
	116 380 216 190 Extension for reply within second month	0.00
1. BASIC FILING FEE Large Entity Small Entity	117 870 217 435 Extension for reply within third month	0.00
Fee Fee Fee Fee Description Fee Paid	118 1,360 218 680 Extension for reply within fourth month	0.00
Code (\$) Code (\$) 101 760 201 380 Utility filing fee	128 1,850 228 925 Extension for reply within fifth month	0.00
106 310 206 155 Design filing fee 690.00	119 300 219 150 Notice of Appeal	0.00
107 480 207 240 Plantfiling fee	120 300 220 150 Filing a brief in support of an appeal	0.00
108 760 208 380 Reissue filing fee	121 260 221 130 Request for or all hearing	0.00
114 150 214 75 Provisional filing fee	138 1,510 138 1,510 Petition to institute a public use proceeding	0.00
	140 110 240 55 Petition to revive - unavoidable	0.00
subtotal (1) (\$) 690.00	141 1,210 241 605 Petition to revive - unintentional	0.00
2. EXTRA CLAIM FEES Fee from	142 1,210 242 605 Utility issue fee (or reissue)	0.00
Ex <u>tra Claims below Fee Paid</u>	143 430 243 215 Design issue fee	0.00
Total Claims 35 -20**= 15	144 580 244 290 Plantissue fee	0.00
Claims — — — — — — — — — — — — — — — — — — —	122 130 122 130 Petitions to the Commissioner	0.00
Multiple Dependent = 0.00 = 0.00 **or number previously paid, if greater; For Reissues, see below	123 50 123 50 Petitions related to provisional applications	0.00
Large Entity Small Entity	126 240 126 240 Submission of Information Disclosure Stmt	0.00
Fee Fee Fee Fee Description Code (\$) Code (\$)	581 40 581 40 Recording each patent assignment per property (times number of properties)	
103 18 203 9 Claims in excess of 20	146 760 246 380 Filing a submission after final rejection	0.00
102 78 202 39 Independent claims in excess of 3	(37 CFR 1.129(a))	0.00
104 260 204 130 Multiple dependent claim, if not paid	149 760 249 380 Foreach additional invention to be examined (37 CFR 1.129(b))	0.00
109 78 209 39 ** Reissue indenpendent claims over original patent	Other fee (specify)	0.00
110 18 210 9 ** Reissue claims in excess of 20 and over original patent	Other fee (specify)	0.00
SUBTOTAL (2) (\$) 348.00	*Reduced by Basic Filing Fee Paid SUBTOTAL (3) (\$) 0.0	

SUBMITTED B	Υ			Complete (if	applicable)
Typed or Printed Name	David G. Latwesen, Ph.D.			Reg. Number	38,533
Signature	Sem	Date	8/31/00	Deposit Account User ID	

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EL465686385 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION FOR LETTERS PATENT

Methods of Removing at Least Some Of A Material from A Semiconductor Substrate

INVENTORS

Kevin J. Torek Garo J. Derderian

ATTORNEY'S DOCKET NO. MI22-1376

Methods of Removing at Least Some Of A Material from A Semiconductor Substrate

TECHNICAL FIELD

The invention pertains to methods of forming and utilizing ozone to remove at least some of a material from a semiconductor substrate. In particular applications, the invention pertains to methods of utilizing organic material vapors in combination with ozone to remove materials from semiconductor substrates.

BACKGROUND OF THE INVENTION

It is common to utilize ozone for removing materials from over semiconductor substrates during semiconductor device fabrication. For instance, ozone can be utilized for removing photoresist and other organic materials. The ozone is typically generated proximate to, or within, a reaction chamber. The semiconductor substrate is provided within the reaction chamber, and the ozone is contacted with the material which is to be removed.

Ozone can be utilized for removing organic materials, such as, for example, photoresist, in that the ozone can oxidize the organic material and thereby convert the organic material to a form which is more readily removed from over a semiconductor substrate than was the organic material prior to oxidation.

A method of forming ozone is to feed a diatomic oxygen (O_2) containing feed gas into an ozone generator. The feed gas is generally about 99.9% O_2 (by volume), with the remaining 0.1% of the feed gas comprising mostly nitrogen (N_2) . Occasionally, additional nitrogen may be spiked into the feed gas to raise a concentration of nitrogen up to about 5%. A reason for utilizing the relatively low purity oxygen as a feed gas for generating ozone is that it can be cheaper than higher purity oxygen. Another reason is that there can be a reduced risk of flame or explosion in utilizing a lower purity oxygen, relative to that which would exist in utilizing a higher purity oxygen.

The invention encompasses new methods of forming and utilizing ozone in removing materials from over semiconductor substrates.

SUMMARY OF THE INVENTION

The invention encompasses a method of removing at least some of a material from a semiconductor substrate. A feed gas is fed through an ozone generator to generate ozone. The feed gas comprises at least 99.999% O₂ (by volume). The ozone, or a fragment of the ozone, is contacted with a material on a semiconductor substrate to remove at least some of the material from the semiconductor substrate.

In another aspect, the invention encompasses another method of removing at least some of a material from a semiconductor substrate.

A mixture of ozone and organic solvent vapors is formed in a reaction

chamber. At least some of the ozone and solvent vapors are contacted with a material on a semiconductor substrate to remove at least some of the material from the semiconductor substrate.

BRIEF DESCRIPTION OF THE DRAWINGS

Preferred embodiments of the invention are described below with reference to the following accompanying drawings.

Fig. 1 is a diagrammatic, cross-sectional view of a reaction chamber utilized in accordance with a method of the present invention.

Fig. 2 is a diagrammatic, cross-sectional, fragmentary view of a semiconductor wafer fragment at a preliminary processing step of a method of the present invention.

Fig. 3 is a view of the Fig. 2 wafer fragment shown at a processing step subsequent to that of Fig. 2.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

This disclosure of the invention is submitted in furtherance of the constitutional purposes of the U.S. Patent Laws "to promote the progress of science and useful arts" (Article 1, Section 8).

Methodology encompassed by the present invention is described with reference to Figs. 1-3. Referring initially to Fig. 1, an apparatus 10 is diagrammatically illustrated. Apparatus 10 comprises a

reaction chamber 12 having a semiconductor wafer support 14 therein.

A semiconductor wafer 16 is shown on support 14.

An ozone generator 18 is shown mounted relative to chamber 12 such that ozone 20 formed within generator 18 is expelled into chamber 12. An exemplary ozone generator is an ASTEX™ 8200, which is manufactured by Applied Science and Technology, of 3500 Cabot Rd, Woburn, Massachusetts. It is to be understood that ozone generator 18 can be mounted outside of chamber 12, and ozone flowed from generator 18 into chamber 12. It is also to be understood that ozone generator 18 could be mounted such that it is fully enclosed within chamber 12. Further, it is to be understood that ozone generator 18 can be mounted above wafer 16, as shown, or can be mounted in other orientations relative to wafer 16.

A feed gas source 22 is provided externally of chamber 12, and a feed gas 24 is flowed from source 22 to ozone generator 18. Feed gas 24 comprises O_2 , and in contrast to the prior art preferably comprises at least 99.999% O_2 (by volume). Feed gas 24 is flowed into ozone generator 18 to form ozone 20. An advantage of utilizing a feed gas with a higher purity of oxygen than the prior art is that such reduces a concentration of nitrogen within the feed gas. In accordance with one aspect of the invention, it is recognized that nitrogen can be converted to various nitrous oxides (NO_x) upon being passed with oxygen through an ozone generator. The nitrous oxides can be corrosive and

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otherwise damaging to integrated circuitry exposed to the nitrous oxides. Further, the nitrous oxides can form various acids (such as, for example, HNO₃) which can be corrosive to various integrated circuitry materials, such as, for example, aluminum oxide (Al₂O₃). Accordingly, one aspect of the invention encompasses utilization of a higher purity oxygen in an ozone-generating feed gas than that which is utilized in the prior art. A related aspect of the invention is that such utilizes an ozone-generating feed gas having less nitrogen than prior art feed gases. Preferably, the ozone-generating feed gas 24 comprises less than or equal to 0.001% N₂ (by volume).

Semiconductor substrate 16 comprises an upper layer 17. Semiconductor substrate 16 can comprise, for example, monocrystalline silicon lightly-doped with a background p-type dopant. To aid in interpretation of the claims that follow, the terms "semiconductive substrate" and "semiconductor substrate" are defined to mean any construction comprising semiconductive material, including, but not limited to, bulk semiconductive materials such as a semiconductive wafer (either alone or in assemblies comprising other materials thereon), and semiconductive material layers (either alone or in assemblies comprising other materials). The term "substrate" refers to any supporting structure, including, but not limited to, the semiconductive substrates described above. Upper layer 17 can comprise, for example, aluminum

oxide (Al₂O₃), platinum, or other materials associated with fabrication of integrated circuitry.

A layer 19 is over upper layer 17 of semiconductor substrate 16, and comprises a material which is to be removed. Layer 19 can comprise, for example, photoresist, such as, for example, a so-called I-line photoresist (typically a novolac resin), or a deep ultraviolet resist. Alternatively, layer 19 can comprise other organic materials.

Ozone 20 is utilized to remove at least some of layer 19 from over semiconductor substrate 16. In other words, the ozone is utilized to remove a material defined by layer 19 from over the upper layer 17 of semiconductor substrate 16.

In one aspect, ozone 20 flows to material 19 to react with the material and form a product which can be removed from over semiconductor substrate 16. For instance, ozone 20 can oxidize an organic material 19 to form a relatively volatile material which can be swept from over layer 17 by flow of gases through reaction chamber 12.

In another aspect, ozone 20 can be broken into reactive fragments which contact material 19 and react with the material to form a product which can be removed from over layer 17. In the shown embodiment, an ultraviolet light source 30 is provided proximate reaction chamber 12 and adjacent a window 32 which extends through a wall of reaction chamber 12. Ultraviolet light generated by source 30 passes through window 32 into chamber 12. The ultraviolet light can then impact

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ozone 20 within chamber 12 to cause the ozone to break into reactive fragments. Such reactive fragments can comprise, for example, atomic oxygen. The fragments formed from the ozone can also comprise O₂. In embodiments in which ozone 20 is exposed to ultraviolet light prior to contact of the ozone or fragments thereof with material 19, such exposure preferably occurs proximate layer 19. In such context, the term "proximate" means that the exposure occurs within one foot of layer 19. Such can alleviate losses of the reactive species formed by the exposure prior to interaction of the reactive species with layer 19. In particular aspects of the invention, the ultraviolet light can be shined onto a surface of layer 19 while the surface is exposed to ozone.

The apparatus 10 of Fig. 1 further comprises a reservoir 50 comprising a volatile material 52. Reservoir 50 is on a reservoir holder 54 which can comprise a heater. In operation, material 52 is volatilized from reservoir 50 to form vapor within reaction chamber 12 which can enhance removal of material 19 by ozone 20. Volatile material 52 can comprise, for example, water, and accordingly the vapor formed within the reaction chamber 12 will be water vapor. Alternatively, volatile material 52 could comprise an organic solvent such as, for example, one or more of cyclohexanone, acetone, or propylene glycol methylether acetate (PGMEA). In particular embodiments, the solvent can consist essentially of, or consist of, acetone. embodiments, the solvent can consist essentially of, or consist of,

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cyclohexanone. In yet other embodiments, the solvent can consist essentially of, or consist of, a mixture of cyclohexanone and PGMEA. A particular solvent can comprise a mixture of 60% cyclohexanone and 40% PGMEA. An alternative solvent is propylene glycol. Although the solvents described above would be liquid materials, it is to be understood that reservoir 50 could also comprise a volatile solid material.

If the material 52 is volatile at a temperature within reactor 12, vapors will be formed from material 52 without additional heating of the material. Alternatively, if material 52 is not volatile at the temperature of reaction chamber 12, or if it desired to enhance volatilization of material 52, the material can be heated by, for example, a heater within support 54.

If material 52 comprises a volatile organic material, then the vapors formed from material 52 will be volatile organic solvent vapors. It is to be understood that within the context of this document the term "solvent vapor" refers to a vapor formed from a volatile organic material, and not to any volatile organic materials formed by degradation of layer 19 within chamber 12. If volatile solvent vapors are utilized in conjunction with the very pure oxygen described above, it is preferred that flames and sparks be kept out of the reaction chamber to alleviate a risk of fire or explosion.

Although reservoir 50 is shown provided within chamber 12, it is to be understood that the invention encompasses other embodiments

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wherein reservoir 50 is provided outside of chamber 12, and wherein solvent vapors are flowed into chamber 12 from the external reservoir. Also, the invention encompasses embodiments wherein vapors are provided in a gas source external of chamber 12 (such as, for example, a tank of gas), and piped into chamber 12.

Organic solvent vapors are found to assist in removal of organic materials 19 (such as, for example, photoresist) from over semiconductor substrates. A possible mechanism is that the vapors may "wet" or otherwise improve susceptibility of an organic material 19 to ozone or reactive fragments formed from ozone. Such mechanism is provided to assist in understanding the present invention, and is not to limit the claims except to the extent that the mechanism is expressly recited within a claim.

Figs. 2 and 3 illustrate enlarged views of the semiconductor substrate 16 at processing steps of a method of the present invention. Fig. 2 illustrates semiconductor substrate 16 having material 19 thereover. Specifically, material 19 is over a layer 17. As discussed above, layer 19 can comprise an organic material such as, for example, photoresist. Layer 17 can comprise an inorganic material such as, for example, aluminum oxide or platinum.

Referring to Fig. 3, semiconductor substrate 16 is illustrated after material 19 has been removed from over layer 17. Such removal can be accomplished by the processing described above with reference to Fig. 1,

wherein ozone (or a reactive fragment generated from ozone) is contacted with material 19 to remove material 19. It is noted that some of layer 17 can be exposed to the ozone, or ozone fragments, during removal of material 19. In accordance with an embodiment of the present invention, the ozone preferably will be formed from an oxygen feed material that comprised less than 0.001% nitrogen. Accordingly, any concentration of nitrous oxides or reactive products formed from nitrous oxides will be lower in methods of the present invention than in prior art processes. Accordingly, if layer 17 comprises aluminum oxide, platinum, or other materials which can be etched or otherwise corroded by nitrous oxide or products thereof, methods of the present invention can alleviate such corrosion relative to prior art methods.

In compliance with the statute, the invention has been described in language more or less specific as to structural and methodical features. It is to be understood, however, that the invention is not limited to the specific features shown and described, since the means herein disclosed comprise preferred forms of putting the invention into effect. The invention is, therefore, claimed in any of its forms or modifications within the proper scope of the appended claims appropriately interpreted in accordance with the doctrine of equivalents.

CLAIMS:

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1. A method of removing at least some of a material from a semiconductor substrate, comprising:

feeding a feed gas through an ozone generator to generate ozone from the feed gas; the feed gas comprising at least 99.999% O_2 (by volume); and

contacting the ozone or a fragment of the ozone with a material on a semiconductor substrate to remove at least some of the material from the semiconductor substrate.

- 2. The method of claim 1 further comprising irradiating at least some of the ozone with ultraviolet light prior to the contacting.
- 3. The method of claim 1 further comprising irradiating at least some of the ozone with ultraviolet light proximate the material.
- 4. The method of claim 1 wherein the material on the semiconductor substrate is photoresist.
- 5. The method of claim 1 further comprising mixing the ozone with water vapor prior to the contacting.

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2	with an organic solvent vapor prior to the contacting.
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4	7. A method of removing at least some of a material from a
5	semiconductor substrate, comprising:
6	feeding a feed gas through an ozone generator to generate ozone
7	from the feed gas; the feed gas comprising O_2 and less than or equal
8	to 0.001% N_2 (by volume); and
9	contacting the ozone or a fragment of the ozone with a material
10	on a semiconductor substrate to remove at least some of the material
11	from the semiconductor substrate.
12	
13	8. The method of claim 7 further comprising irradiating at least
14	some of the ozone with ultraviolet light prior to the contacting.
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16	9. The method of claim 7 wherein the material on the
17	semiconductor substrate is photoresist.
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19	10. The method of claim 7 further comprising mixing the ozone
20	with water vapor prior to the contacting.
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6. The method of claim 1 further comprising mixing the ozone

	11	. The	method	of	claim 7	fu	rther	comprising	mixing	the	ozone
with	an	organic	solvent	vapo	r prior	to	the	contacting.			

12. A method of removing at least some of a material from a semiconductor substrate, comprising:

forming a mixture of ozone and organic solvent vapors in a reaction chamber; and

contacting at least some of the ozone and solvent vapors with a material on a semiconductor substrate to remove at least some of the material from the semiconductor substrate.

- 13. The method of claim 12 wherein the material on the semiconductor substrate is photoresist.
- 14. The method of claim 12 wherein the material on the semiconductor substrate is photoresist; wherein the semiconductor substrate comprises Al₂O₃; and further comprising exposing at least some of the Al₂O₃ to the ozone during the contacting.

15. The method of claim 12 wherein the material on the semiconductor substrate is photoresist; wherein the semiconductor substrate comprises platinum; and further comprising exposing at least some of the platinum to the ozone during the contacting.

- 16. The method of claim 12 further comprising providing a reservoir of volatile organic solvent within the reaction chamber and forming the solvent vapors from the volatile organic solvent.
- 17. The method of claim 16 wherein the volatile organic solvent is a liquid.
- 18. The method of claim 16 wherein the volatile organic solvent comprises acetone.
- 19. The method of claim 16 wherein the volatile organic solvent consists essentially of acetone.
- 20. The method of claim 16 wherein the volatile organic solvent comprises cyclohexanone.

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21. T	the method	l of clain	1 16	wherein	the	volatile	organic	solvent
consists essen	tially of c	zclohevan	one					

- The method of claim 16 wherein the volatile organic solvent 22. comprises a mixture of cyclohexanone and PGMEA.
- 23. The method of claim 16 wherein the volatile organic solvent comprises propylene glycol.
- The method of claim 12 further comprising providing a 24. reservoir of volatile organic solvent within the reaction chamber and heating the volatile organic solvent to form the solvent vapors from the volatile organic solvent.

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25. A method of removing at least some of a material from a semiconductor substrate, comprising:

forming a mixture of ozone and organic solvent vapors in a reaction chamber;

irradiating at least some of the ozone with ultraviolet light to form ozone fragments from the ozone; and

contacting at least some of the ozone fragments and solvent vapors with a material on a semiconductor substrate to remove at least some of the material from the semiconductor substrate.

- 26. The method of claim 25 wherein the material on the semiconductor substrate is photoresist.
- 27. The method of claim 25 further comprising providing a reservoir of volatile organic solvent within the reaction chamber and forming the solvent vapors from the volatile organic solvent.
- 28. The method of claim 27 wherein the volatile organic solvent is a liquid.
- 29. The method of claim 27 wherein the volatile organic solvent comprises acetone.

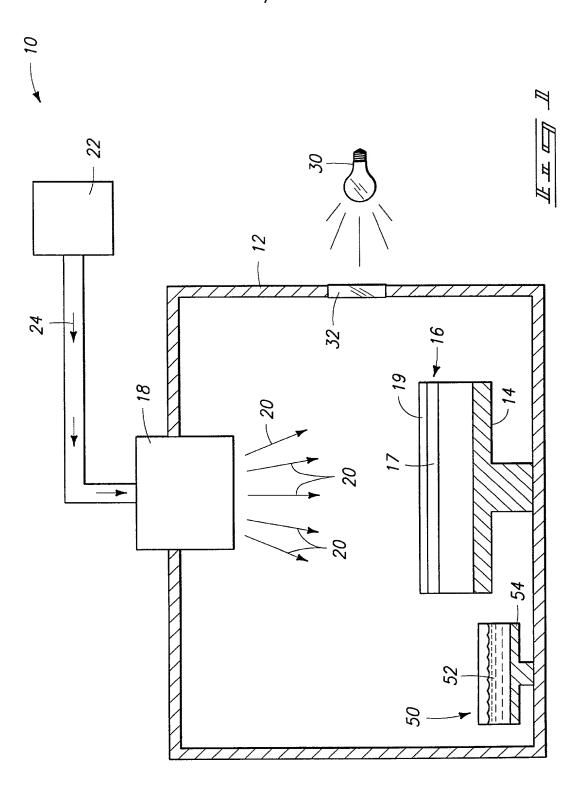
30.	The method of claim 27 wherein the volatile organic solvent
comprises c	yclohexanone.
31.	The method of claim 27 wherein the volatile organic solvent
comprises a	mixture of cyclohexanone and PGMEA.
32.	The method of claim 27 wherein the volatile organic solvent
comprises p	propylene glycol.
33.	The method of claim 25 further comprising providing a
reservoir of	f volatile organic solvent within the reaction chamber and

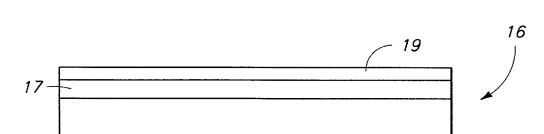
- 33. The method of claim 25 further comprising providing a reservoir of volatile organic solvent within the reaction chamber and heating the volatile organic solvent to form the solvent vapors from the volatile organic solvent.
- 34. The method of claim 25 wherein the material on the semiconductor substrate is photoresist; wherein the semiconductor substrate comprises Al_2O_3 ; and further comprising exposing at least some of the Al_2O_3 to the ozone fragments during the contacting.

35. The method of claim 25 wherein the material on the semiconductor substrate is photoresist; wherein the semiconductor substrate comprises platinum; and further comprising exposing at least some of the platinum to the ozone fragments during the contacting.

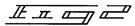
ABSTRACT OF THE DISCLOSURE

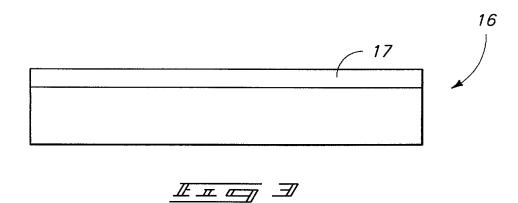
The invention encompasses a method of removing at least some of a material from a semiconductor substrate. A feed gas is fed through an ozone generator to generate ozone. The feed gas comprises at least 99.999% O₂ (by volume). The ozone, or a fragment of the ozone, is contacted with a material on a semiconductor substrate to remove at least some of the material from the semiconductor substrate. The invention also encompasses another method of removing at least some of a material from a semiconductor substrate. A mixture of ozone and organic solvent vapors is formed in a reaction chamber. At least some of the ozone and solvent vapors are contacted with a material on a semiconductor substrate to remove at least some of the material from the semiconductor substrate.





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DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: Methods of Removing at Least Some of a Material from a Semiconductor Substrate, the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations §1.56.

PRIOR FOREIGN APPLICATIONS:

I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so

1	made are punishable	by fine or imprisonment,	or both, under
2	Section 1001 of Title 18	8 of the United States Code ar	nd that such willful
3	false statement may je	eopardize the validity of the	application or any
4	patent issued therefrom	1.	
5		* * * * * * * * *	
6	Full name of inventor:	Kevin J. Torek	
7	Inventor's Signature:		-
8	Date:		
9	Residence:	Meridian, ID	
10	Citizenship:	US	
11	Post Office Address:	1718 N. Kastle Falls Ave.	
12		Meridian, ID 83642	
13		* * * * * * * *	
14	Full name of inventor:	Garo J. Derderian	
15	Inventor's Signature: _		-
16	Date:		
17	Residence:	Boise, ID	
18	Citizenship:	US	
19	Post Office Address:	6184 S. Schooner Place Boise, ID 83716	
20		Doise, ID 03/10	
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